

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,961,111 B1  
APPLICATION NO. : 09/831315  
DATED : November 1, 2005  
INVENTOR(S) : Keizaburo Kuramasu et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:


On the title page, column 1, section (75), add inventor --Yutaka Nanno, Takarazuka (JP)--.

On the title page, column 2, section (56), add Other Documents subsection, add citation -- Inoue et al. "4.2: Low-Temperature Poly-Si TFT-LCD with Integrated 6-bit Digital Data Drivers," 1996, pages 21-24--.

In claim 2, column 26, line 16, replace "comprising" with --comprises--.

Signed and Sealed this

Nineteenth Day of September, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*